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10/815,453

March 31, 2004

Ricardo E. Gonzalez

2183

Unknown

PA2718US

ENCLOSURES (Check all that apply)

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<input type="checkbox"/> Reply to Missing Parts/ Incomplete Application	<input type="checkbox"/> Landscape Table on CD	4) List of Co-Pending Applications;
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Remarks		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Carr & Ferrell LLP Cust. No. 22830		
Signature			
Printed name	Eugene G. Kim		
Date	April 26, 2005	Reg. No.	46,267

CERTIFICATE OF TRANSMISSION/MAILING

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Signature			
Typed or printed name	Eugene G. Kim, Reg. No. 46,267		Date April 26, 2005

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Ricardo E. Gonzalez et al.

APPLICATION NO.: 10/815,453

FILED: March 31, 2004

TITLE: Configuring a Multi-Processor System

EXAMINER: Unknown

GROUP ART UNIT: 2183

ATTY.DKT.NO.: PA2718US

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INFORMATION DISCLOSURE STATEMENT
Under 37 C.F.R. §§ 1.56, and 1.97-1.98

SIR:

Pursuant to the provisions of 37 C.F.R. §§ 1.56 and 1.97-98 of the Rules of Practice in Patent Cases, enclosed herewith is form PTO-SB-08 listing several references and a list of co-pending applications; copies of the non-patent references are enclosed. The Examiner is requested to make these references of official record in the application. The references cited may be material to examination of the application and are submitted in compliance with Applicant(s)'s duty of disclosure as defined by 37 C.F.R. § 1.56.

No representation is made or intended as to the completeness of this list, nor is the inclusion of any reference on this list an admission that it is prior art or pertinent to this application.

Applicants believe no fee is due with this submission. If a fee is due, however, the Commissioner is hereby authorized to charge any necessary fee to Account Number 06-0600. A copy of this statement is submitted in duplicate for this purpose.

Respectfully submitted,
Ricardo E. Gonzalez et al.

Dated: April 26, 2005

By: 

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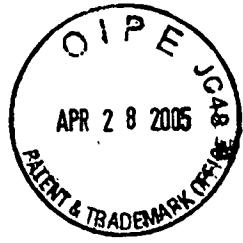
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Dated: April 26, 2005

By: 

Eugene G. Kim, Reg. No. 46,267



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**List of Co-Pending Patent Applications That May Be Directed
Towards Similar Subject Matter**

Examiner's Initials	Serial Number	First-Named Inventor	Title	Filing Date
/M.M./	10/750,714	Ricardo E. Gonzalez	Systems and Methods for Software Extensible Multi-Processing	12/31/2003
/M.M./	11/021,247	Ricardo E. Gonzalez	Systems and Methods for Selecting Input/Output Configuration in an Integrated Circuit	12/21/2004
Examiner's Signature:	/Marshall Mcleod/			Date 04/14/2008



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EXAMINER: Unknown

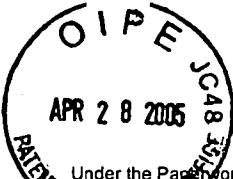
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Examiner's Signature:	/Marshall Mcleod/		Date	04/14/2008



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Submitted Form 1449/PTO

Complete if Known

Application Number	10/815,453				
Filing Date	March 31, 2004				
First Named Inventor	Ricardo E. Gonzalez				
Art Unit	2183				
Examiner Name	Unknown				
Sheet	1	of	1	Attorney Docket Number	PA2718US

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/M.M./		GOLDBLATT, K., "The Low-Cost, Efficient Serial Configuration of Spartan FPGAs," XAPPO98, November 13, 1998 (Version 1.0), XILINX.	
/M.M./		SCOTT, S. et al., "The Cray T3E Network: Adaptive Routing in a High Performance 3D Torus," HOT Interconnects IV, August 15-16, 1996, Stanford University.	
/M.M./		GOLESTARI, S.J., "A Stop-and-Go Queuing Framework for Congestion Management," Applications, Technologies, Architectures, and Protocols for Computer Communication, Proceedings of the ACM Symposium on Communications Architectures & Protocols, 1990, ACM Press, New York, NY USA.	
/M.M./		DALLY, W. et al., "Deadlock Free Message Routing in Multiprocessor Interconnection Networks," Computer Science Department, California Institute of Technology, May 10, 1985.	
/M.M./		GARLAND, D. et al., "An Introduction to Software Architecture," Advances in Software Engineering and Knowledge Engineering, 1993, Vol. 1, World Scientific Publishing Company, New Jersey, USA.	

Examiner Signature	/Marshall Mcleod/	Date Considered	04/14/2008
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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